

ABOUT THE PRES

The Pravara Rural Education Society was established in July 1964 by **Late. Padmashri Dr. Vitthalrao Vikhe Patil**, a great visionary, philanthropist & pioneer of first cooperative sugar factory in cooperative sector not only in India but Asia. The objective was to avail the technical education in the rural area at affordable cost. **Late. Padmabhushan Balasaheb Vikhe Patil**, an outstanding leader in Maharashtra & a member of Parliament for over three decades, embarked upon the arduous task of translating the dream of Padmashri into concrete realities by meticulous planning. It symbolizes the ascendance of academic excellence, quality education from preprimary to research level.

ABOUT THE INSTITUTE

Pravara Rural Education Society established Sir Visvesvaraya Institute of Technology in 1998-99. It offers Under Graduate Engineering Courses in Chemical Engineering, Computer Engineering, Electrical Engineering, E&TC Engineering, Mechanical Engineering, Information Technology & P.G. Courses in Mechanical Engineering, E&TC Engineering, Computer Engineering and MBA. The institute is running under the dynamic leadership of **Hon'ble Shri. Radhakrishna Vikhe Patil**, Leader of opposition, Govt. of Maharashtra, to equip the institute with excellent infrastructure & with modern methods of governance.

The institute has well qualified, devoted and dedicated team of faculty members. Conducive environment exists between staff and students. Quality technical education is provided to the students to meet challenges of globalization with state of art equipment and laboratories.

ABOUT E&TC DEPARTMENT

Electronics and Telecommunication Department is established in the year 2002-03. The department is well known for excellent infrastructure, with developed and well-furnished laboratories, experienced and dedicated faculty members. Department has state of art laboratories for subjects like VLSI, Embedded System, Communication etc. & Computer Center with the recent software which enables to conduct value addition courses like VLSI Design, MATLAB, Embedded System Design, etc. are organized in addition to curriculum activity.

ABOUT THE WORKSHOP

The department of Electronics and Telecommunication Engineering conducted the various Sponsored State Level Workshops under QIP, SPPU, AICTE etc. on Advancement in Embedded System, Recent trends in Technology, Robotics, WSN-NS2, LaTeX etc. and also conducted Non-Sponsored workshop in association with Texas Instruments, Cadence, ISTE, IETE etc as a part of routine academic activities. We organizes a State Level Workshop for improving the knowledge and skills of the faculty to make them acquainted with the conceptual as well as practical knowledge of the latest technologies. The program enables participants to develop competency in research and development with advanced technology.

The program is basically intended to motivate the current and prospective faculty members and researchers of Communication and other working professionals.

The Program marked with various eminent speakers of the country those dedicated their decade of years at research in communication, automation, control, embedded, WSN etc. The eminent Speakers are invited from reputed organization such as IIT's, NIT's, DRDO, BHEL, IETE etc. to contribute their views in workshop and provide the platform to the researchers to have direct interaction with the speakers and to enhance their skill through the practical approach and hands-on practice during the sessions.

This workshop will impart knowledge from experts on 5G Multi Antenna technologies and enables them to be updated and knowledgeable with this state of art technologies. It should help to motivate students as well as faculties to put more emphasis on 5G Multi-Antenna System to provide a wide variety of mobile services and application in communication system with resolving issues and challenges in 5G technology.

ELIGIBILITY OF PARTICIPATIONS

Faculty from AICTE approved Engineering College in the following discipline of Electronics / Electronics and Telecommunication / Industrial Electronics / Electrical Engineering / Electrical & Electronics Engineering / Computer Science / Instrumentation / Information Technology.

IMPORTANT DATES

Dead line for Registration : **December 25, 2018**

Intimation of acceptance : **December 31, 2018**

Please confirm registration to mail ID:

rana.mahajan@pravara.in

A State Level Workshop

On "5G Multi-Antenna Technology"

January, 17-18 2019



**Sir Visvesvaraya Institute of Technology,
Chincholi, Nasik**

REGISTRATION FORM

1. Name : _____

2. Organization : _____

3. Office/College Address: _____
Pin: _____

4. Contact No. (R): _____ (O) : _____

5. Email-ID: _____

6. Residential Address: _____
Pin: _____

DETAILS OF REGISTRATION

Please find enclosed herewith a DD No. / Cheque No. / Cash _____ Dated / / worth Rs: _____ Drawn on: _____ in favor of **PRINCIPAL, Sir Visvesvaraya Institute of Technology, Chincholi**, payable at Nasik.

Download registration form: www.svitnashik.in

Online registration: <https://tinyurl.com/SVIT-workshop>

Date: / /2018.

Note: 1. Registration Fees (All Members): Rs.100/-

2. Accommodation facility provided to outstation participants on prior request at nominal charges.

Signature

CONVENER

Mr. Ulhas V. Patil
HOD, E&TC Engineering

ORGANIZING COMMITTEE

Mr.Rana S. Mahajan
Ms.Snehal Somwanshi
Mr.Liladhar P. Bhamare
Ms.Archana A. Hatkar
Ms.Rajshree Shinde
Ms.Pruna Autade
Ms.Gauri Varade
Mr.Prashant Bibave
Ms.Trupti Wable

CONTACT PERSON

Mr.Rana. S. Mahajan 9096339024
Ms.Snehal Somwanshi 7709346206
Mr.Liladhar P. Bhamare 9404565473

DIRECTION TO REACH

- **Location:** The Institute is located on the bus link Nasik- Pune Highway, 12 Km from Nasik & 9 Km from Nasik Road Railway Station. 12 Km from Sinner Bus Stand & 70Kms from Shirdi of Saibaba.
- Institute Bus is available and can avail the facility on prior request basis from Nashik Road.



SVIT, Nashik

CHIEF PATRON

Hon'ble Namdar Shri. Radhakrishna Vikhe Patil
(Leader of opposition, Govt. of Maharashtra)

PATRON

Hon'ble Namdar Sau. Shalinitai Vikhe Patil
(President, Zilha Parishad, Ahmednagar)

Hon'ble Shir. Annasaheb Mhaske Patil
(Trustee, P.R.E.S., Loni)

Hon'ble Dr. Rajendra E. Vikhe Patil
(Chief Executive Officer,
Pro. Chancellor of Pravara Deemed University,
Trustee & Secretary, Pravara Medical Trust, Loni)

Hon'ble Dr.Sujaydada Vikhe Patil
(Chairman, Pravara Sugar factory, Pravaranagar)

Hon'ble Shri. Abasaheb G. Kharde Patil
(Secretary & Trustee, PRES, Loni)

Hon'ble Dr. Sarjerao B. Nimse
(Director General, PRES, Pravaranagar)

Dr. Ashok Kolhe
(Chief Executive Officer, PRES, Pravaranagar)

Prof. (Dr). K.T.V. Reddy
(Campus Director & Principal SVIT)

ADVISORY COMMITTEE

Dr. Vishwas B. Gaikwad
(Dean, Faculty of Science & Technology,
SPPU, Pune)

Dr. Bormane Dattatraya Shankar
(BOS E&TC, SPPU, Pune)

Dr. S. Pal
(Formal VC , DIAT, Pune)

Dr. K. P. Kumar
(DGM, BSNL, Mumbai)

Dr.S.N.Marchant
EE Dept., IIT, Bombay

Dr.Vimal Wakhula
Tata Teleservices, CEO, New Delhi



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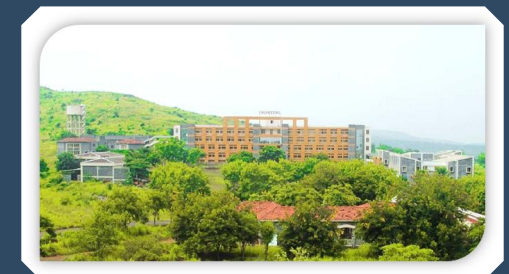
- Sponsored by -

Savitribai Phule Pune University, Pune

- Organized by -

Electronics & Telecommunication Engineering

Sir Visvesvaraya Institute of Technology, Nashik



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