Electronics And Telecommunication Engineering

Details of Elective options offered by the university and those opted by the Institute

Academic Year	Semester	Name of the Class	Name of Elective Subject	Number of students Opted
2017-18	Sem 1	**M.E. II** VLSI and Embedded Syatem (2013 Course)	**Elective –III** 1.Value Education, Human Rights and Legislative Procedures 2. Environmental Studies 3. Energy Studies 4. Disaster Management 5. Knowledge Management 6. Foreign Language 7. Economics for Engineers 8. Engineering Risk – Benefit Analysis 9. Technology Play 10. Optimization Techniques 11. Fuzzy Mathematics 12. Design and Analysis of Algorithms 13. CUDA	1. CUDA=03
2017-18	Sem 1	**M.E. I** VLSI and Embedded System (2017Course)	**Elective –I** 1. Micro Electromechanical Systems 2. Nano Technology 3. Processor Design 4. Wireless Sensor Networks 5. MOS Device Modeling and Characterization	1. Processor Design=04
2017-18	Sem 2	**M.E. I** VLSI and Embedded System (2017Course)	**Elective –II** 1. Embedded Product Design 2. High Speed ICs 3. Mixed Signal IC Design 4. Embedded Signal Processor Architectures 5. Real Time Operating Systems	1. Embedded Product Design=04

M.E. coordinator
Prof.A.A.Hatkar

Principal
Sir Visvesvaraya Institute of Technology
Chincholi,Nasik-422102

HOD ETC
Prof.U.V.Patil

H.O.D.
Department of Electronics & Telecommunication Engg.
SVIT, Tal. Sinnar,
Dist. Nashik

Name of Subject :- Processor Design.

Roll No	Title	First Name	Fathers Name	Surname
1	Ms	Pushpalata	Nivruttirao	Sali
4	Ms	Payal	Gopinath	Vanhere
5	Mr	Nitin	Ramdas	Jadhav
6	Mr	Raoshaeb	Gulabrao	Chavan

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Roll No	Title	First Name	Fathers Name
1	Miss	Tejasvini	Ashok
2	Miss	Jyoti	Prakash
3	Mr	Nilesh	Kisan

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